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IEE JNL IEE Journal or Magazine

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IEEE STD IEEE Standard

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- ☐ 1. **A fast IP-core integration methodology for SoC design**
 Filho, J.Ad.'O.; de Lima, M.E.; Maciel, P.R.; Moura, J.; Celso, B.;
Integrated Circuits and Systems Design, 2003. SBCCI 2003. Proceedings. 16th
 8-11 Sept. 2003 Page(s):131 - 136
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- #3 ((protocol conversion<in>metadata) <and> (system on
chip<in>metadata))<and> (integrated circuit<in>metadata)
- #4 ((protocol conversion<in>metadata) <and> (system on
chip<in>metadata))<and> (integrated circuit<in>metadata)
- #5 ((protocol conversion<in>metadata) <and> (system on
chip<in>metadata))<and> (integrated circuit<in>metadata)
- #6 ((protocol conversion<in>metadata) <and>
(asic<in>metadata))<and> (integrated circuit or
ic<in>metadata)

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L2	3941566	(sos system near2 chip asic application adj specific adj integrated adj circuit) (single adj2 (ic intergrated adj circuit))	US-PGPUB; USPAT	OR	ON	2006/04/17 10:09
L3	3941244	(sos system near2 chip asic application adj specific adj integrated adj circuit) (single adj2 (ic intergrated adj circuit)) with protocol adj conversion	US-PGPUB; USPAT	OR	ON	2006/04/17 10:10
L4	3941244	(sos system near2 chip asic application adj specific adj integrated adj circuit) (single adj2 (ic intergrated adj circuit)) with protocol near conver\$5	US-PGPUB; USPAT	OR	ON	2006/04/17 10:10
L5	269	((sos system near2 chip asic application adj specific adj integrated adj circuit) (single adj2 (ic intergrated adj circuit))) with protocol adj conversion	US-PGPUB; USPAT	OR	ON	2006/04/17 10:16
L6	3941244	(sos system near2 chip asic application adj specific adj integrated adj circuit) (single adj2 (ic intergrated adj circuit)) with protocol near2 conver\$5	US-PGPUB; USPAT	OR	ON	2006/04/17 10:10
L7	159	5 and @ad<"20010904"	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/04/17 10:11
L8	3	5 not so	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/04/17 10:16

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